National Semiconductor

LM195/LM395 Ultra Reliable Power Transistors

General Description

The LM195/LM395 are fast, monolithic power transistors with complete overload protection. These devices, which act as high gain power transistors, have included on the chip, current limiting, power limiting, and thermal overload protection making them virtually impossible to destroy from any type of overload. In the standard TO-3 transistor power package, the LM195 will deliver load currents in excess of 1.0A and can switch 40V in 500 ns.

The inclusion of thermal limiting, a feature not easily available in discrete designs, provides virtually absolute protection against overload. Excessive power dissipation or inadequate heat sinking causes the thermal limiting circuitry to turn off the device preventing excessive heating.

The LM195 offers a significant increase in reliability as well as simplifying power circuitry. In some applications, where protection is unusually difficult, such as switching regulators, lamp or solenoid drivers where normal power dissipation is low, the LM195 is especially advantageous.

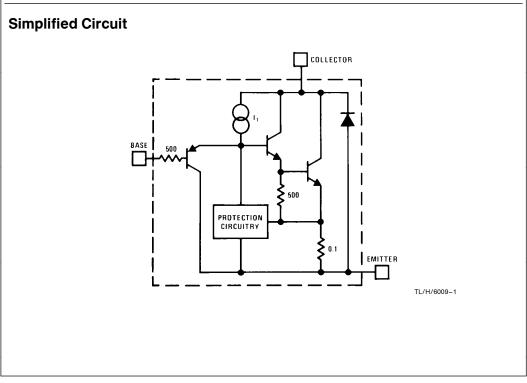
The LM195 is easy to use and only a few precautions need be observed. Excessive collector to emitter voltage can destroy the LM195 as with any power transistor. When the device is used as an emitter follower with low source impedance, it is necessary to insert a 5.0k resistor in series with the base lead to prevent possible emitter follower oscillations. Although the device is usually stable as an emitter follower, the resistor eliminates the possibility of trouble without degrading performance. Finally, since it has good high frequency response, supply bypassing is recommended.

For low-power applications (under 100 mA), refer to the LP395 Ultra Reliable Power Transistor.

The LM195/LM395 are available in standard TO-3 power packages and solid Kovar TO-5. The LM195 is rated for operation from -55°C to $+150^\circ\text{C}$ and the LM395 from 0°C to $+125^\circ\text{C}$.

Features

- Internal thermal limiting
- Greater than 1.0A output current
- 3.0 µA typical base current
- 500 ns switching time
- 2.0V saturation
- Base can be driven up to 40V without damage
- Directly interfaces with CMOS or TTL
- 100% electrical burn-in

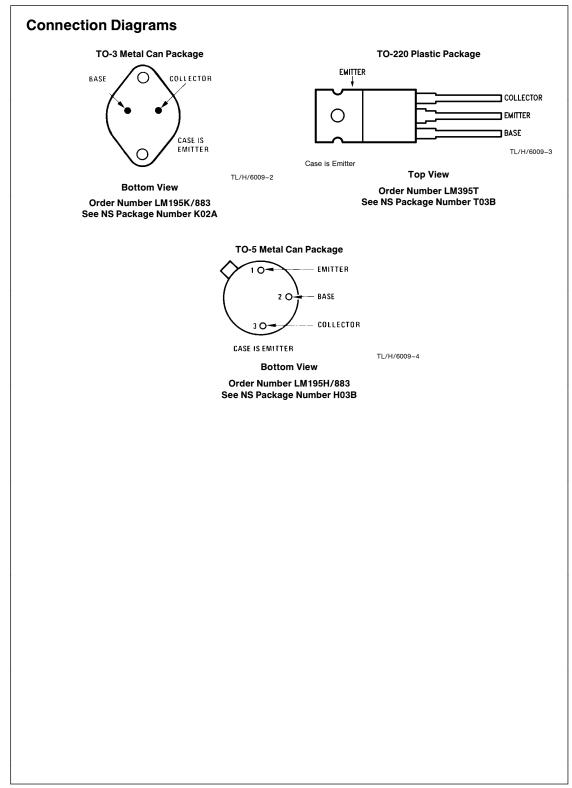


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LM195/LM395 Ultra Reliable Power Transistors

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Absolute Maximum Ratings

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/Distributors for availability and specifications. Collector to Emitter Voltage

Collector to Efficient Voltage	
LM195	42V
LM395	36V
Collector to Base Voltage	
LM195	42V
LM395	36V
Base to Emitter Voltage (Forward)	
LM195	42V
LM395	36V

Electrical Characteristics (Note 1)

Base to Emitter Voltage (Reverse)	20V
Collector Current	Internally Limited
Power Dissipation	Internally Limited
Operating Temperature Range LM195 -55°C to +150°C	
LM195 LM395	-55°C to +150°C 0°C to +125°C
Storage Temperature Range	$-65^{\circ}C$ to $+150^{\circ}C$
Lead Temperature (Soldering, 10 sec.)	260°C

Preconditioning

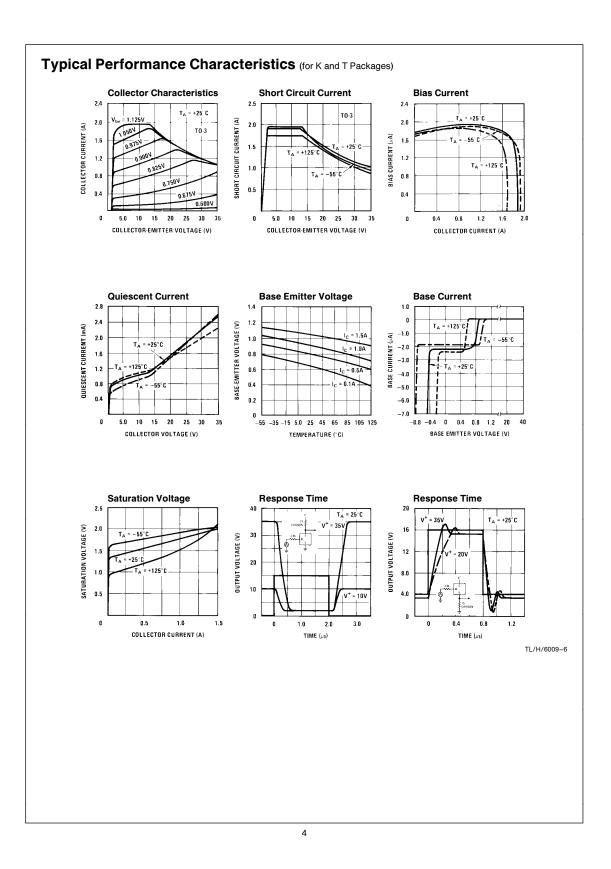
100% Burn-In In Thermal Limit

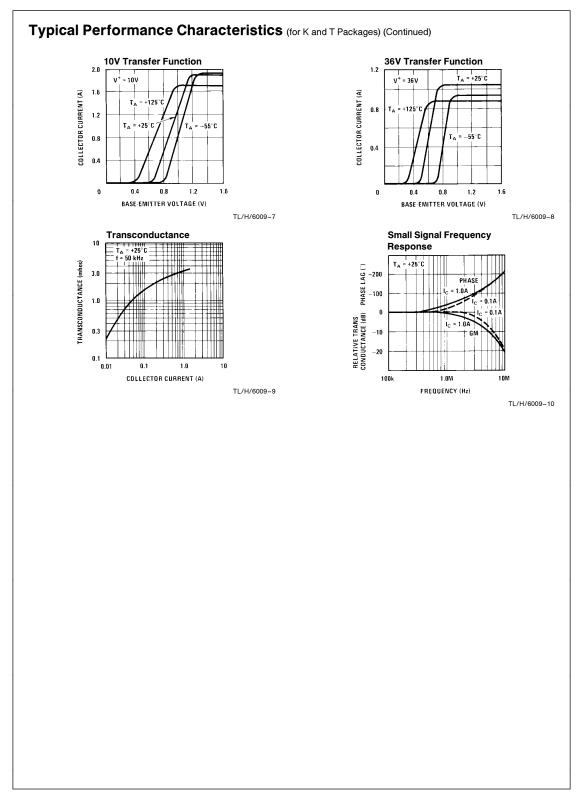
LM195 LM395 Conditions Units Parameter Min Тур Max Min Тур Мах Collector-Emitter Operating Voltage $I_Q \leq I_C \leq I_{MAX}$ v 42 36 (Note 3) Base to Emitter Breakdown Voltage $0 \leq V_{CE} \leq V_{CEMAX}$ 42 36 60 ٧ Collector Current TO-3, TO-220 $V_{CE} \leq 15V$ 1.2 2.2 1.0 2.2 А TO-5 $V_{CE} \leq 7.0V$ 1.2 1.0 1.8 1.8 А ٧ Saturation Voltage $I_C \leq$ 1.0A, $T_A =$ 25°C 1.8 2.0 1.8 2.2 Base Current $0 \leq I_C \leq I_{MAX}$ 3.0 5.0 3.0 10 μA $0 \leq V_{CE} \leq V_{CEMAX}$ $V_{be} = 0$ Quiescent Current (IQ) 2.0 5.0 2.0 10 mΑ $0 \leq V_{CE} \leq V_{CEMAX}$ Base to Emitter Voltage $I_{C} = 1.0A, T_{A} = +25^{\circ}C$ 0.9 0.9 v Switching Time $V_{CE}=36V, R_L=36\Omega,$ 500 500 ns $T_A = 25^{\circ}C$ °C/W Thermal Resistance Junction to TO-3 Package (K) 2.3 3.0 2.3 3.0 Case (Note 2) °C/W TO-5 Package (H) 12 15 12 15 TO-220 Package (T) °C/W 6 4

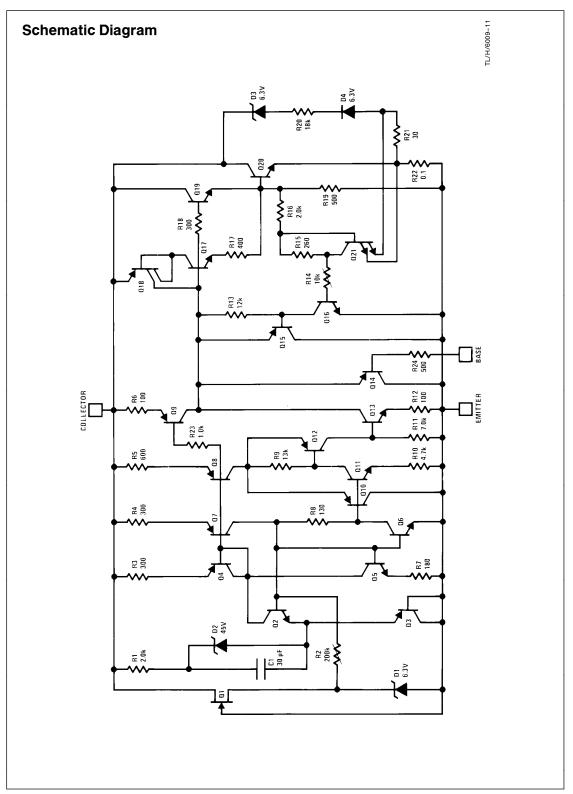
Note 1: Unless otherwise specified, these specifications apply for $-55^{\circ}C \le T_j \le +150^{\circ}C$ for the LM195 and $0^{\circ}C \le +125^{\circ}C$ for the LM395. Note 2: Without a heat sink, the thermal resistance of the TO-5 package is about $+150^{\circ}C/W$, while that of the TO-3 package is $+35^{\circ}C/W$.

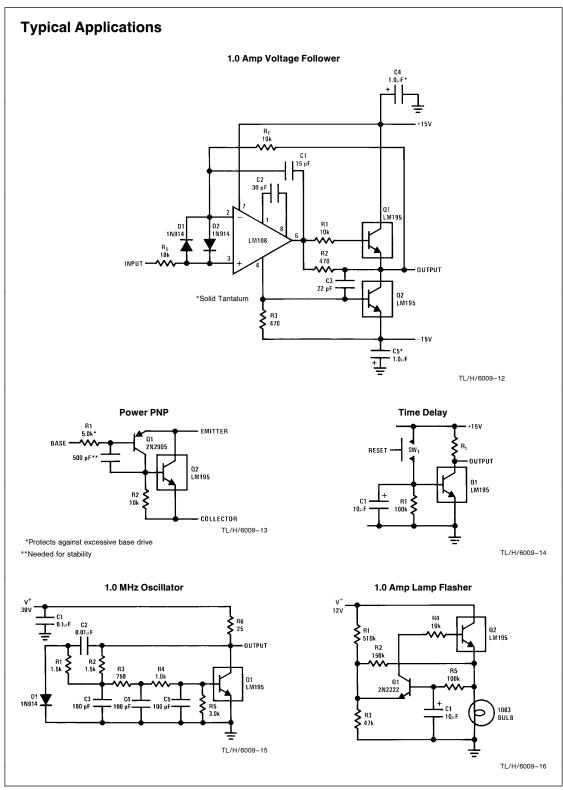
Note 3: Selected devices with higher breakdown available.

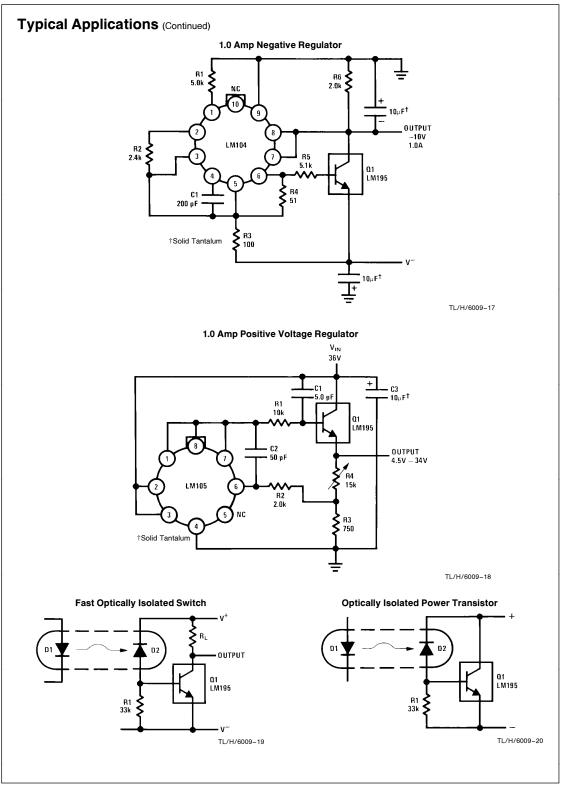
Note 4: Refer to RETS195H and RETS195K drawings of military LM195H and LM195K versions for specifications.

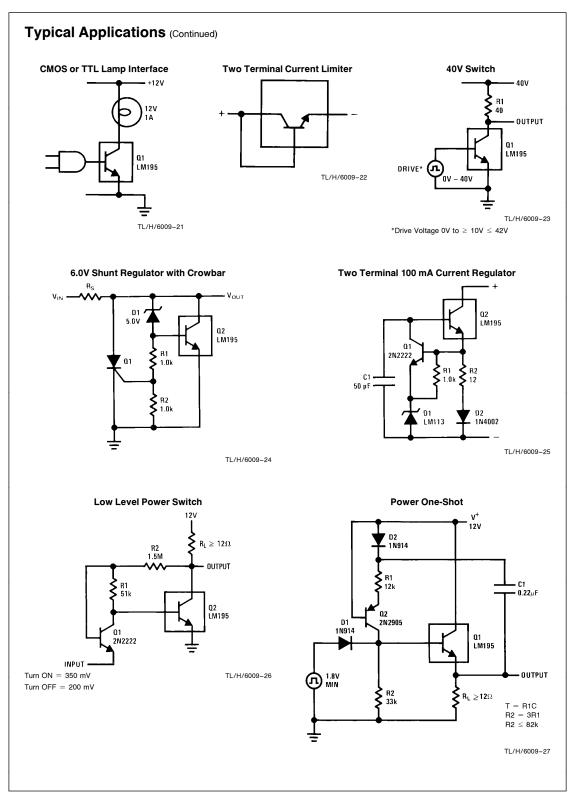


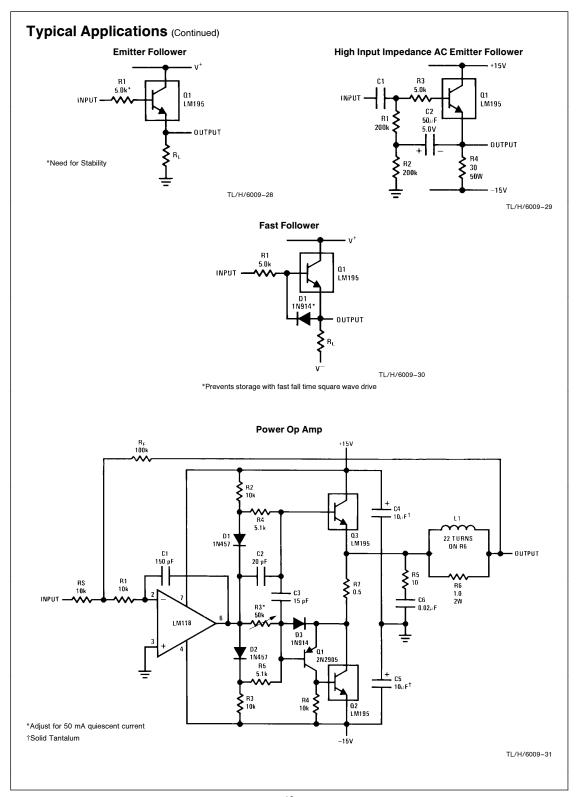


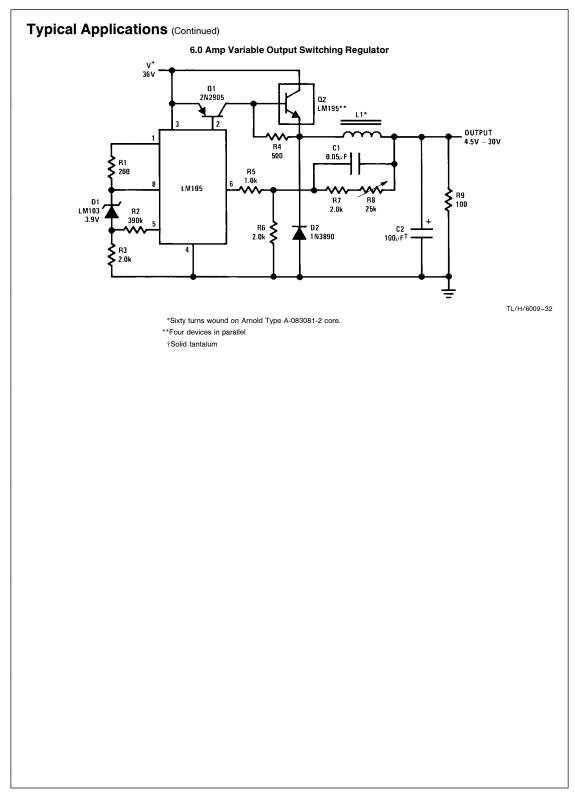


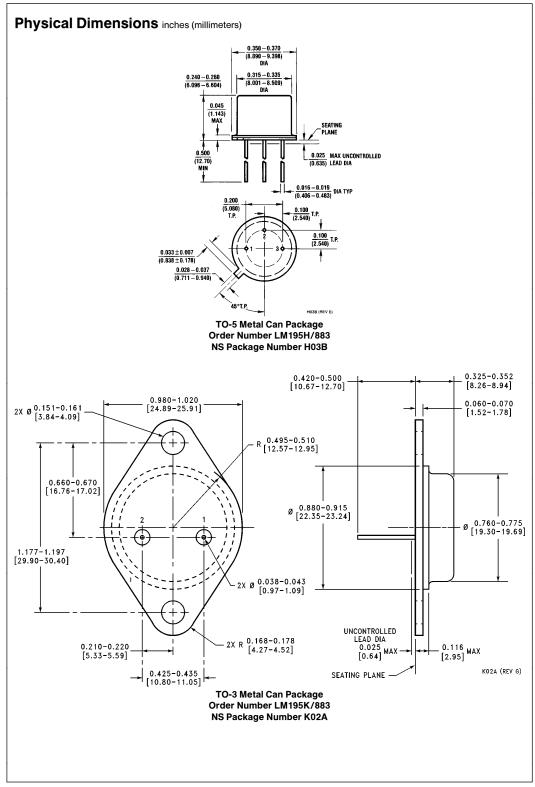


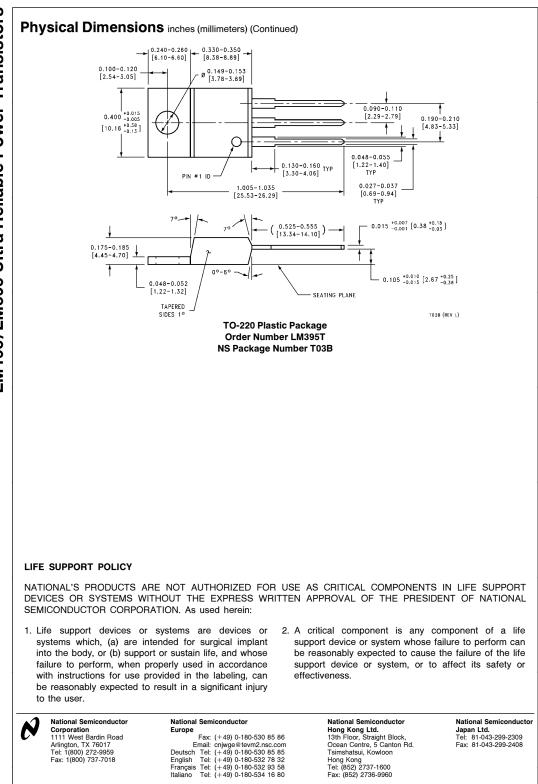












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